

## AMENDMENTS TO THE SPECIFICATION:

Please amend paragraph [01] as follows:

[01] ~~[Not Applicable]~~ This application is a continuation of Patent Application No. 10/164,205 filed June 6, 2002 titled "Multi-Level Single-Ended Input Level Shifter Circuit" (Attorney Docket No. 13546US01), the complete subject matter of which is incorporated herein by reference in its entirety.

Please amend the Abstract of the Disclosure as follows:

~~Systems and methods~~ Methods are disclosed for ~~a multi-level level shifter circuit having translating or shifting a voltage level of a single ended input and adapted to translate one or more signals from one voltage level to another.~~ More specifically, the present invention provides a ~~level shifter~~ method of translating or shifting a voltage level that doesn't require a complementary input or an additional power supply if the complementary signal isn't available. One embodiment of the ~~level shifter circuit device having a single ended input comprises at least three transistor devices. The first transistor device is coupled to at least the input and is adapted to have a threshold voltage less than 0V. The second transistor device is coupled to at least the first transistor device, while a level shifter transistor device is coupled to at least the first and second transistor devices.~~ method of translating a voltage level of a single-ended input signal using at least one native transistor device having a threshold voltage less than 0V comprises outputting a first voltage level if the single ended input signal is in a first state. A second voltage level is output if the single ended input is in a second state.